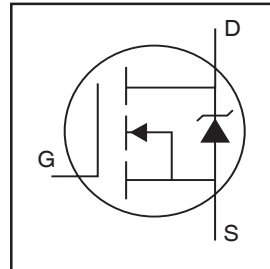


IRF3710PbF

HEXFET® Power MOSFET

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free



$$V_{DS} = 100V$$

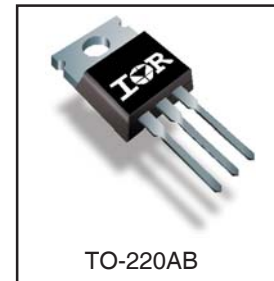
$$R_{DS(on)} = 23m\Omega$$

$$I_D = 57A$$

Description

Advanced HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.



TO-220AB

Absolute Maximum Ratings

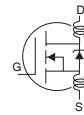
	Parameter	Max.	Units
I_D @ $T_C = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	57	A
I_D @ $T_C = 100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	40	
I_{DM}	Pulsed Drain Current ①	180	
P_D @ $T_C = 25^\circ C$	Power Dissipation	200	W
	Linear Derating Factor	1.3	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
I_{AR}	Avalanche Current①	28	A
E_{AR}	Repetitive Avalanche Energy①	20	mJ
dv/dt	Peak Diode Recovery dv/dt ③	5.8	V/ns
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 175	°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	
	Mounting torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	0.75	°C/W
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50	—	
$R_{\theta JA}$	Junction-to-Ambient	—	62	

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	100	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.13	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	23	m Ω	$V_{GS} = 10V, I_D = 28A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
g_{fs}	Forward Transconductance	32	—	—	S	$V_{DS} = 25V, I_D = 28A$ ④
I_{DSS}	Drain-to-Source Leakage Current	—	—	25	μA	$V_{DS} = 100V, V_{GS} = 0V$
		—	—	250		$V_{DS} = 80V, V_{GS} = 0V, T_J = 150^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20V$
Q_g	Total Gate Charge	—	—	130	nC	$I_D = 28A$
Q_{gs}	Gate-to-Source Charge	—	—	26		$V_{DS} = 80V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	—	43		$V_{GS} = 10V$, See Fig. 6 and 13
$t_{d(on)}$	Turn-On Delay Time	—	12	—	ns	$V_{DD} = 50V$
t_r	Rise Time	—	58	—		$I_D = 28A$
$t_{d(off)}$	Turn-Off Delay Time	—	45	—		$R_G = 2.5\Omega$
t_f	Fall Time	—	47	—		$V_{GS} = 10V$, See Fig. 10 ④
L_D	Internal Drain Inductance	—	4.5	—	nH	Between lead, 6mm (0.25in.) from package and center of die contact
L_S	Internal Source Inductance	—	7.5	—		
C_{iss}	Input Capacitance	—	3130	—	pF	$V_{GS} = 0V$
C_{oss}	Output Capacitance	—	410	—		$V_{DS} = 25V$
C_{rss}	Reverse Transfer Capacitance	—	72	—		$f = 1.0\text{MHz}$, See Fig. 5
E_{AS}	Single Pulse Avalanche Energy②	—	1060③	280⑥		$I_{AS} = 28A, L = 0.70\text{mH}$

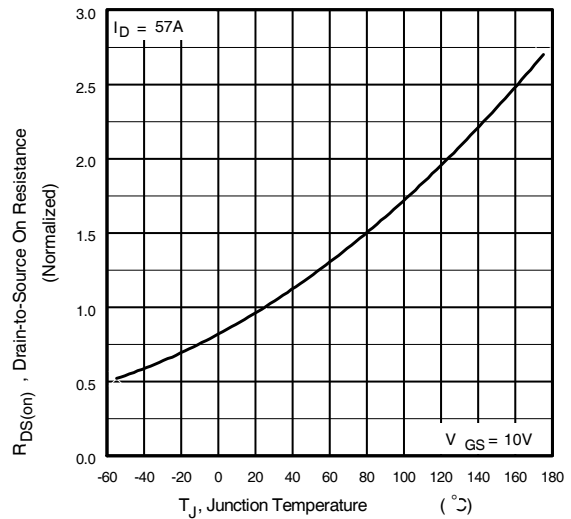
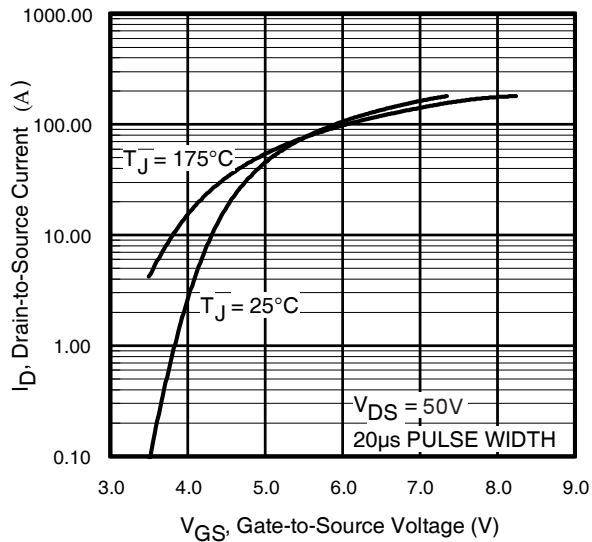
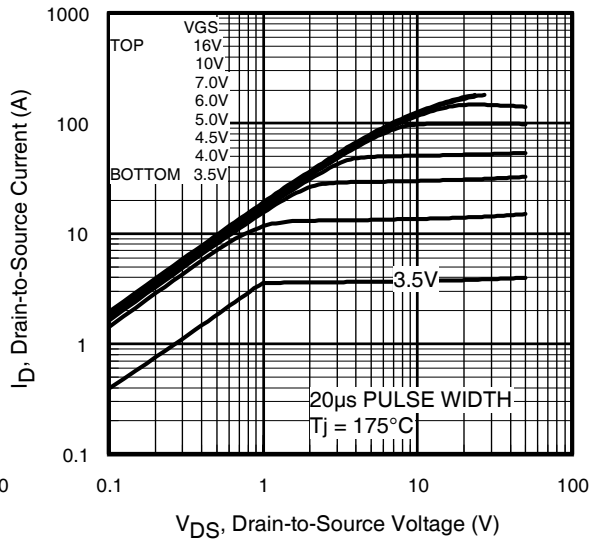
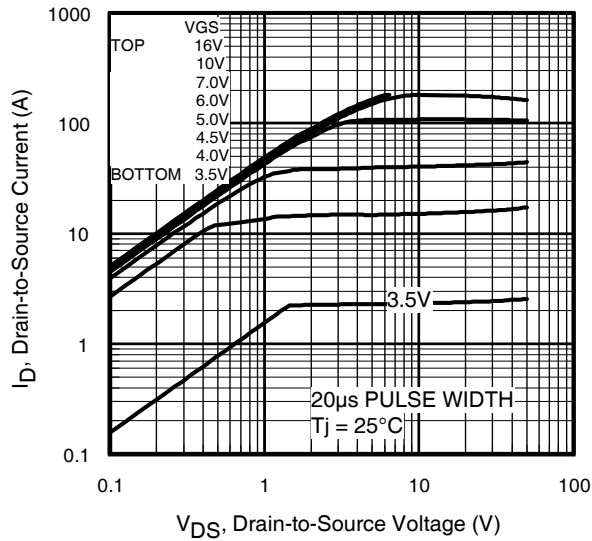


Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	57	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode)①	—	—	230		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}, I_S = 28A, V_{GS} = 0V$ ④
t_{rr}	Reverse Recovery Time	—	140	220	ns	$T_J = 25^\circ\text{C}, I_F = 28A$
Q_{rr}	Reverse Recovery Charge	—	670	1010	nC	$di/dt = 100A/\mu s$ ④
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by $L_S + L_D$)				

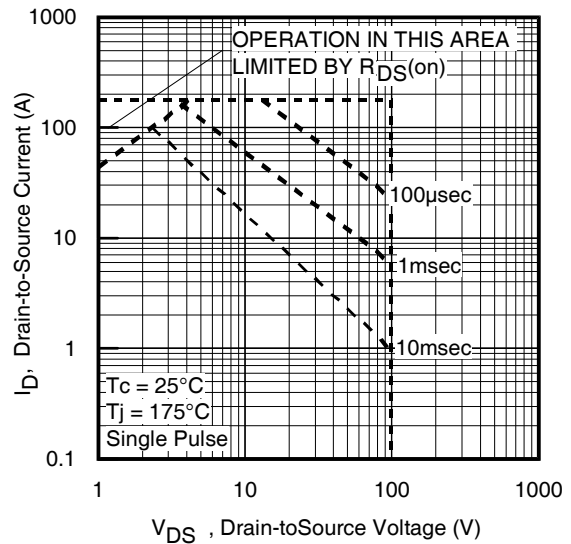
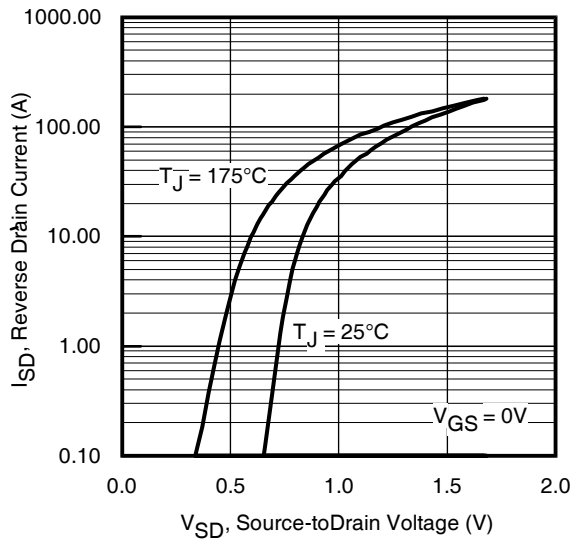
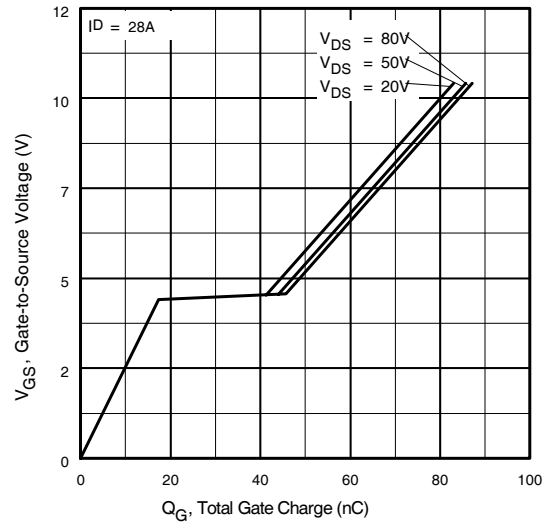
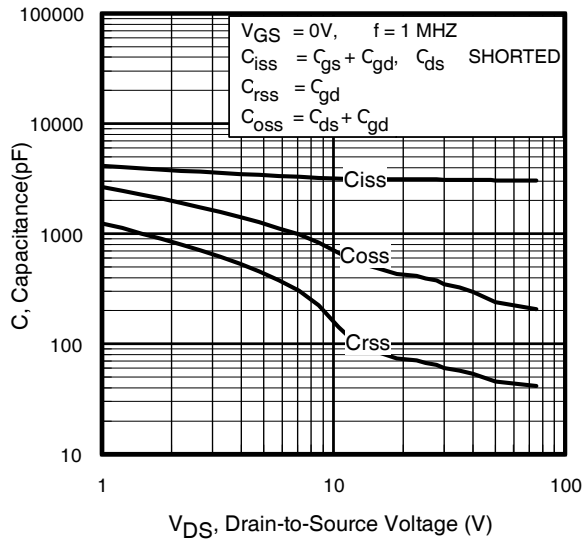
Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- ② Starting $T_J = 25^\circ\text{C}$, $L = 0.70\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 28A$, $V_{GS} = 10V$ (See Figure 12)
- ③ $I_{SD} \leq 28A$, $di/dt \leq 380A/\mu s$, $V_{DD} \leq V_{(BR)DSS}$,
 $T_J \leq 175^\circ\text{C}$
- ④ Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.
- ⑤ This is a typical value at device destruction and represents operation outside rated limits.
- ⑥ This is a calculated value limited to $T_J = 175^\circ\text{C}$.



IRF3710PbF

International
IR Rectifier



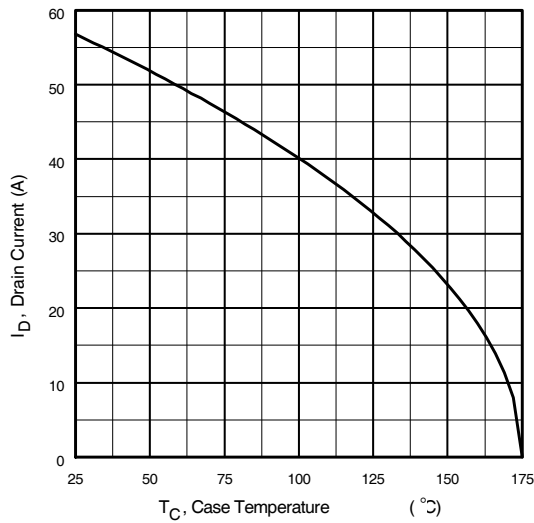


Fig 9. Maximum Drain Current Vs. Case Temperature

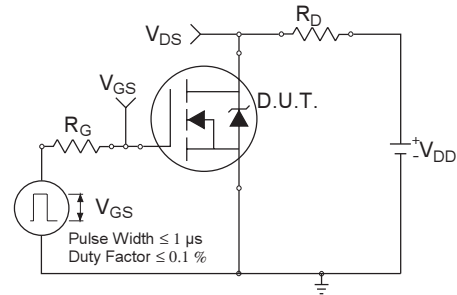


Fig 10a. Switching Time Test Circuit

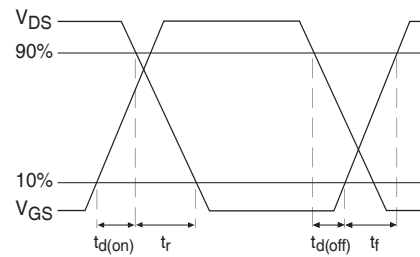


Fig 10b. Switching Time Waveforms

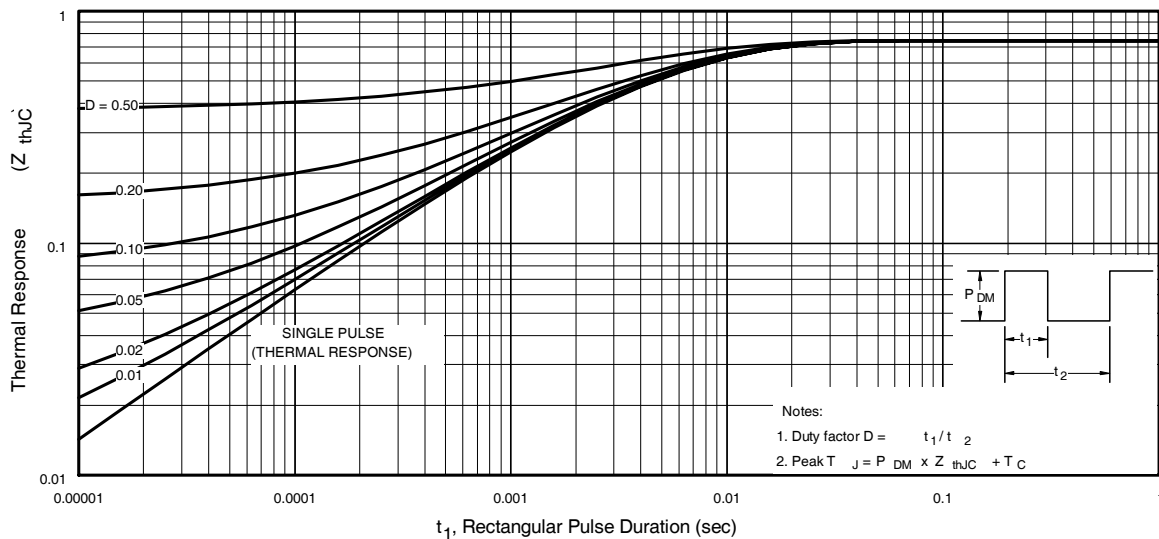


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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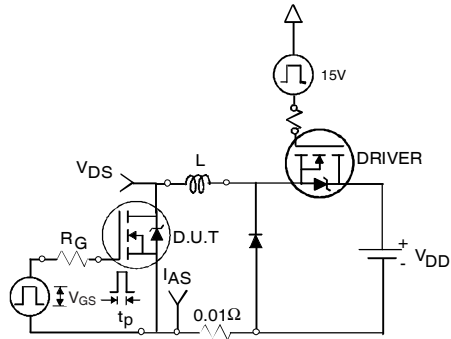


Fig 12a. Unclamped Inductive Test Circuit

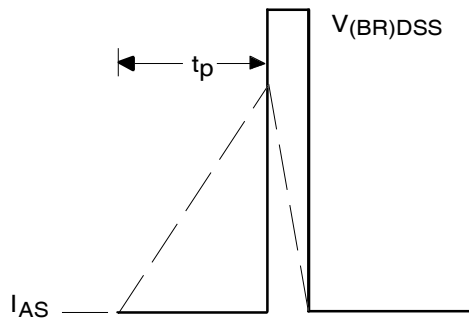


Fig 12b. Unclamped Inductive Waveforms

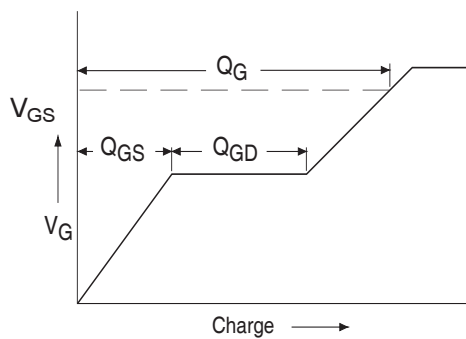


Fig 13a. Basic Gate Charge Waveform

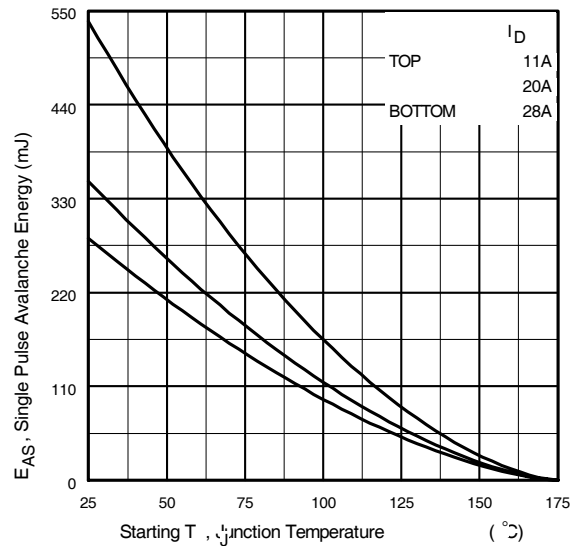


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

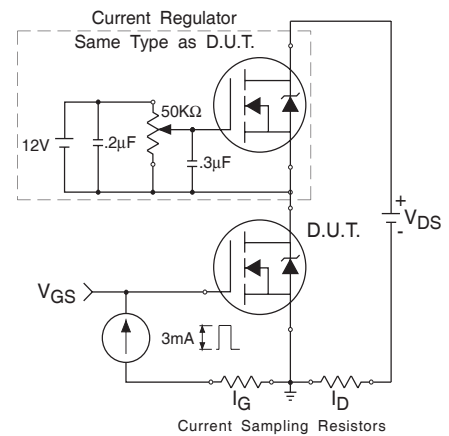
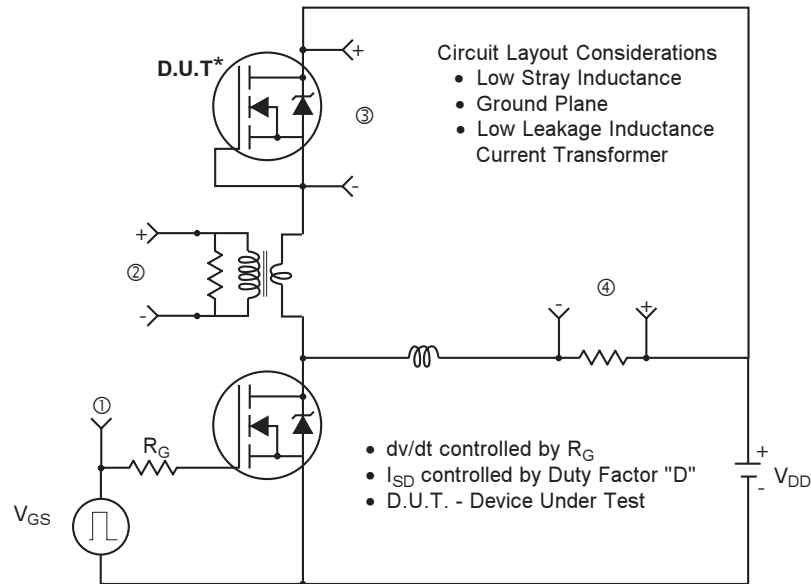
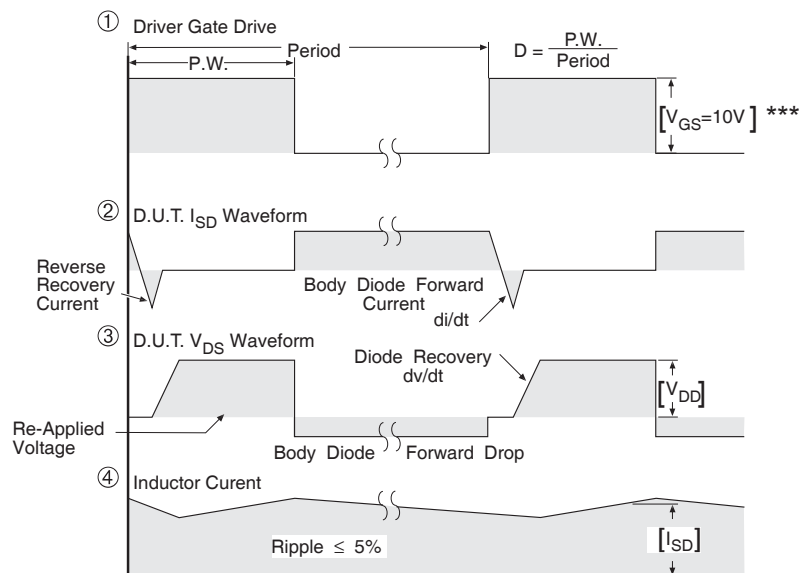


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



* Reverse Polarity of D.U.T for P-Channel



*** $V_{GS} = 5.0V$ for Logic Level and 3V Drive Devices

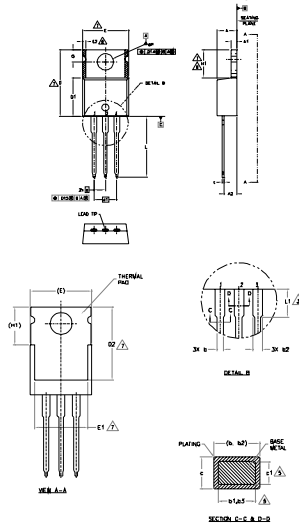
Fig 14. For N-channel HEXFET® power MOSFETs

IRF3710PbF

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TO-220AB Package Outline

Dimensions are shown in millimeters (inches)



- NOTES:
1. DIMENSIONS AND TOLERANCING AS PER ASME Y14.5M-1994
 2. DIMENSIONS ARE SHOWN IN INCHES (MILLIMETERS)
 3. LEAD DIMENSIONS AND FROM UNCONTROLLED IN 1.
 4. DIMENSION G, DI & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .007 (0.175) FOR ANY. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMITY OF THE PLASTIC BODY.
 5. DIMENSION H, I, J & K APPLY TO BASE METAL ONLY.
 6. CONTROLLING DIMENSION - INCHES
 7. THERMAL PAD CONTROL OPTIONAL. OTHER DIMENSIONS EXIST & IT
 8. DIMENSION L2 IS IN TOTAL & ZONE BUMP PLATING.
 9. DIMENSION L3 IS IN TOTAL & ZONE BUMP PLATING.
 10. DIMENSION L4 IS IN TOTAL & ZONE BUMP PLATING.
 11. DIMENSION L5 IS IN TOTAL & ZONE BUMP PLATING.
 12. DIMENSION L6 IS IN TOTAL & ZONE BUMP PLATING.
 13. DIMENSION L7 IS IN TOTAL & ZONE BUMP PLATING.
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 15. DIMENSION L9 IS IN TOTAL & ZONE BUMP PLATING.
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 17. DIMENSION L11 IS IN TOTAL & ZONE BUMP PLATING.
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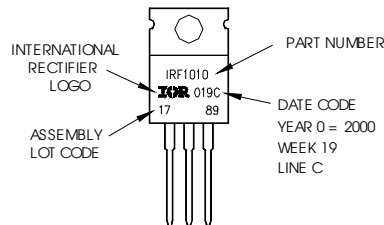
SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	3.56	.63	.140	.390	
A1	0.51	1.01	.020	.060	
A2	2.03	2.28	.080	.115	.045
B	0.38	1.01	.015	.040	
b	0.38	0.97	.015	.038	5
b1	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.36	0.81	.014	.032	
c1	0.36	0.56	.014	.022	5
D	14.22	16.51	.560	.650	4
D1	8.38	9.52	.330	.375	
D2	11.68	12.86	.460	.507	7
E	9.83	10.87	.380	.429	4,7
E1	8.96	8.89	.350	.350	7
E2	-	0.76	-	.030	8
F	2.54 ± .01		.100 ± .002		
F1	0.84	0.86	.033	.034	7,8
F2	12.70	14.73	.500	.580	
L	12.70	14.73	.500	.580	
L1	3.56	4.06	.140	.160	3
L2	1.54	1.54	.061	.061	
L3	2.54	3.42	.100	.135	

ALL DIMENSIONS
IN INCHES
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TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010
LOT CODE 1789
ASSEMBLED ON WW 19, 2000
IN THE ASSEMBLY LINE "C"

Note: "P" in assembly line position
indicates "Lead-Free"



TO-220AB package is not recommended for Surface Mount Application

Notes:

1. For an Automotive Qualified version of this part please see <http://www.irf.com/product-info/auto/>
2. For the most current drawing please refer to IR website at <http://www.irf.com/package/>

Data and specifications subject to change without notice.
This product has been designed and qualified for the Industrial market.
Qualification Standards can be found on IR's Web site.

International
IR Rectifier

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TAC Fax: (310) 252-7903

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